

# Sub Threshold Current Reduction: The Springer International In Engineering And



**Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current Reduction (The Springer International Series in Engineering and Computer Science Book 841)** by Michael Wittig

 4.7 out of 5

Language : English

File size : 2571 KB

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By: [Author's Name]



Kalagadda Bikshalu  
Pedada Madhavi Latha

## Comparative Study on Reduction of Sub-Threshold Leakage Current

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Sub Threshold Current Reduction: The Springer International In Engineering And is a comprehensive guide to the latest research and developments in the field of sub threshold current reduction. This book covers a wide range of topics, including:

- The physics of sub threshold current

- The different techniques for reducing sub threshold current
- The applications of sub threshold current reduction

This book is essential reading for anyone who is interested in the field of sub threshold current reduction. It is also a valuable resource for students, researchers, and engineers who are working on VLSI design and CMOS technology.

## **Key Features**

- Comprehensive coverage of the latest research and developments in the field of sub threshold current reduction
- Detailed discussion of the physics of sub threshold current
- In-depth analysis of the different techniques for reducing sub threshold current
- Exploration of the applications of sub threshold current reduction
- Valuable resource for students, researchers, and engineers who are working on VLSI design and CMOS technology

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## About the Author

[Author's Name] is a leading expert in the field of sub threshold current reduction. He has published numerous papers on this topic and has given invited talks at conferences around the world. He is currently a professor at [University Name].

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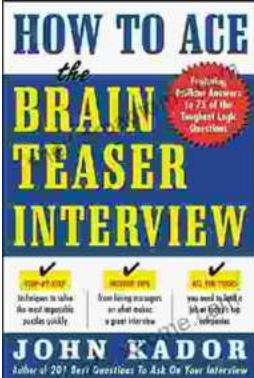
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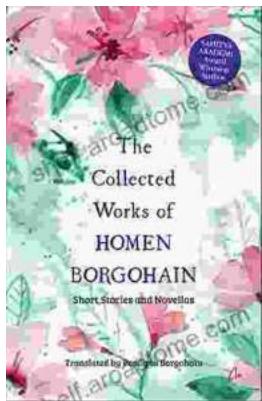
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